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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

SUBMITTAL OF PROPOSED DRAWING CHANGE

APPLICANT:

Gerald DEBOY et al.

GROUP ART UNIT:

2815

SERIAL NO.:

09/530,553

EXAMINER:

P. Brock I

FILING DATE:

July 21, 2000

INVENTION:

HIGH VOLTAGE RESISTANT EDGE STRUCTURE FOR

SEMICONDUCTOR COMPONENTS

Hon. Assistant Commissioner for Patents Washington D.C. 20231

SIR:

Enclosed is a copy of Figure 1 marked in red with a proposed change. In particular, to add the reference character 10' to Figure 1. Approval of the change is hereby requested.

Respectfully submitted,

Melvin A. Robinson (Reg. No. 31,870)

Schiff Hardin & Waite Patent Department 6600 Sears Tower

Chicago, Illinois 60606 Telephone: 312-258-5785

ATTORNEY FOR APPLICANT

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to:

The Assistant Commissioner for Patents

Washington, D.C. 20231

on April 18, 2002.

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